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| **[monnier](http://phys-ds.physics.lsa.umich.edu/docushare/dsweb/Services/User-70" \o "Click to view properties)** Posted: 02/21/04 02:53 PM | |  |  |  | | --- | --- | --- | | Topic | [**New Bulletin Board**](http://phys-ds.physics.lsa.umich.edu/docushare/dsweb/View/Bulletin-12?init=true) | [Properties](http://phys-ds.physics.lsa.umich.edu/docushare/dsweb/Services/Bulletin-12) |   Please use this forum to give status reports on the testing of Leach Electronics. |
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|  | [**epedrett**](http://phys-ds.physics.lsa.umich.edu/docushare/dsweb/Services/User-72) Posted: 03/13/04 05:37 PM | |  |  |  | | --- | --- | --- | | Topic | [**Progress report on Leach electronics testing.**](http://phys-ds.physics.lsa.umich.edu/docushare/dsweb/View/Bulletin-13?init=true) | [Properties](http://phys-ds.physics.lsa.umich.edu/docushare/dsweb/Services/Bulletin-13) |   We have installed Red-Hat 9.0 on the MIRC PC. An initial problem of the RH installer complaining about bad blocks has been solved by not testing the bad blocks during installation (used Western Digital diagnostic instead). The astroPCI driver was succesfully installed. JAVA runtime environment and SDK were also installed (RPMs downloaded from SUN) . These were needed for the VOODOO software installation. The test of the camera acquisition system was succesful. The PC can talk to the camera and download fits frames. |
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|  | [**epedrett**](http://phys-ds.physics.lsa.umich.edu/docushare/dsweb/Services/User-72) Posted: 03/24/04 04:20 PM | |  |  |  | | --- | --- | --- | | Topic | [**Leach and IR-labs electronics test**](http://phys-ds.physics.lsa.umich.edu/docushare/dsweb/View/Bulletin-14?init=true) | [Properties](http://phys-ds.physics.lsa.umich.edu/docushare/dsweb/Services/Bulletin-14) |   The test on the PICNIC electronics continues. Some data was acquired with a text fixture shorting the 4 ADC inputs to ground. A postcript image from the acquired noise is available on this site. Using a sample of 9 frames I measured a noise of 1.18 +/- 0.01 ADU. I originally acquired 10 frames but one of the frame was returned with all zeroes!!! I measured bias signals and clocks fom Leachs electronics. Results in the lab's logbook. The bias voltages look spiky during data acquisition, with oscilloscope triggering at 490 mv. This could not be a problem if happening outside the sampling of the ADC. Measured Clocks. Clocks are always present at the output of the connector. I measured a jitter of .1 us on FSYNC (results in logbook). I also attempted a test with the pre-amplifier connected after checking the cable. Connecting the OA inputs to GND, the ADCs read all at 65535 (+ve saturation). Looking at the circuit I see that a bias is applied to the -ve input of each amplifier. I connect a signal present in the biases with the same value and espect to see noise around zero. I find instead all zeros in the acquired file (maybe -ve saturation ?) |